

## C81 GPIO Description

Please write GPIO driver under OS (windows refer to Microsoft driver program, Linux, Unix refer to Linux, Unix driver program frame mode), use GPIO driver program to drive C19 GPIO1 module.

C81 GPIO1 module is combined with 5 group (10) header pin (refer to motherboard), number 1, 2, 3, 4, 5, 6, 7, 8 pin draw out Simple GPIO, pin 9 is VDD, pin 10 is GND, the singular number pin (1, 3, 5, 7) defaulted as signal input pin, even number (2, 4, 6, 8) defaulted as signal output pin. They can be changed by BIOS setting(if effective)

| Pin | Input | Output | Defaulted Value | Remark                 |
|-----|-------|--------|-----------------|------------------------|
| 1   | •     |        | High            |                        |
| 2   |       | •      | Low             | Depend on BIOS Setting |
| 3   | •     |        | High            |                        |
| 4   |       | •      | Low             | Depend on BIOS Setting |
| 5   | •     |        | High            |                        |
| 6   |       | •      | Low             | Depend on BIOS Setting |
| 7   | •     |        | High            |                        |
| 8   |       | •      | Low             | Depend on BIOS Setting |
| 9   |       |        |                 | GND                    |
| 10  |       |        |                 | VCC                    |

The IO memory address corresponding to GPIO is (only operated by the driver in the OS):

| Pin | offset | location | read/write                               | remark |
|-----|--------|----------|------------------------------------------|--------|
| 1   | 0xA00  | bit0     | read only<br>(depend on BIOS setting)    |        |
| 2   | 0xA02  | bit6     | read / write<br>(depend on BIOS setting) |        |
| 3   | 0xA01  | bit3     | read only<br>(depend on BIOS setting)    |        |
| 4   | 0xA02  | bit7     | read / write<br>(depend on BIOS setting) |        |
| 5   | 0xA07  | bit0     | read only<br>(depend on BIOS setting)    |        |
| 6   | 0xA04  | bit1     | read / write<br>(depend on BIOS setting) |        |
| 7   | 0xA07  | bit1     | read only<br>(depend on BIOS setting)    |        |
| 8   | 0xA04  | bit2     | read / write<br>(depend on BIOS setting) |        |
| 9   |        |          |                                          | GND    |
| 10  |        |          |                                          | VCC    |